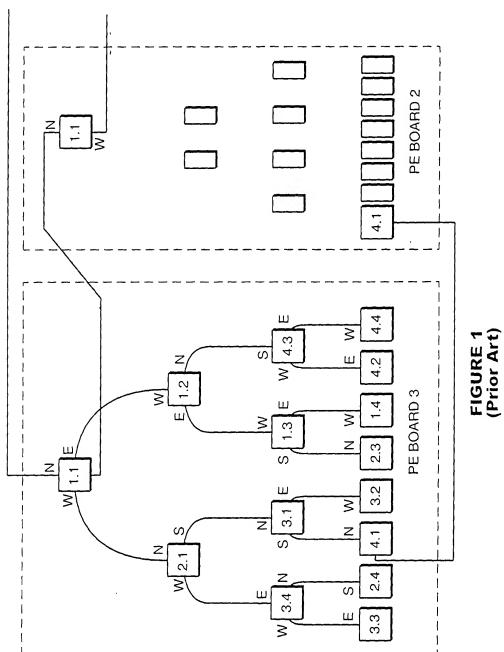
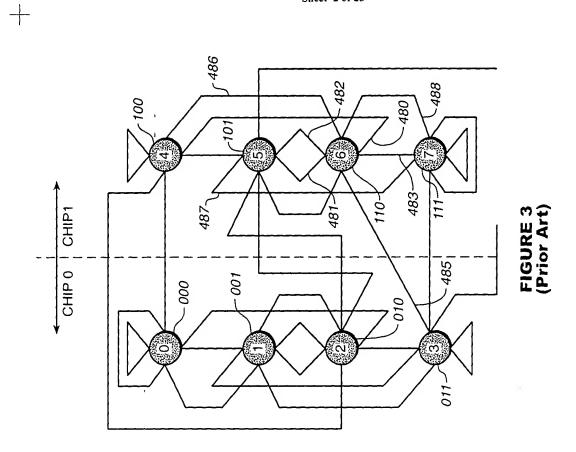
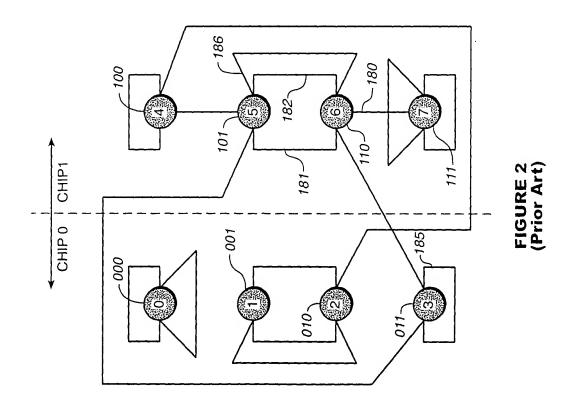
Sheet 1 of 25

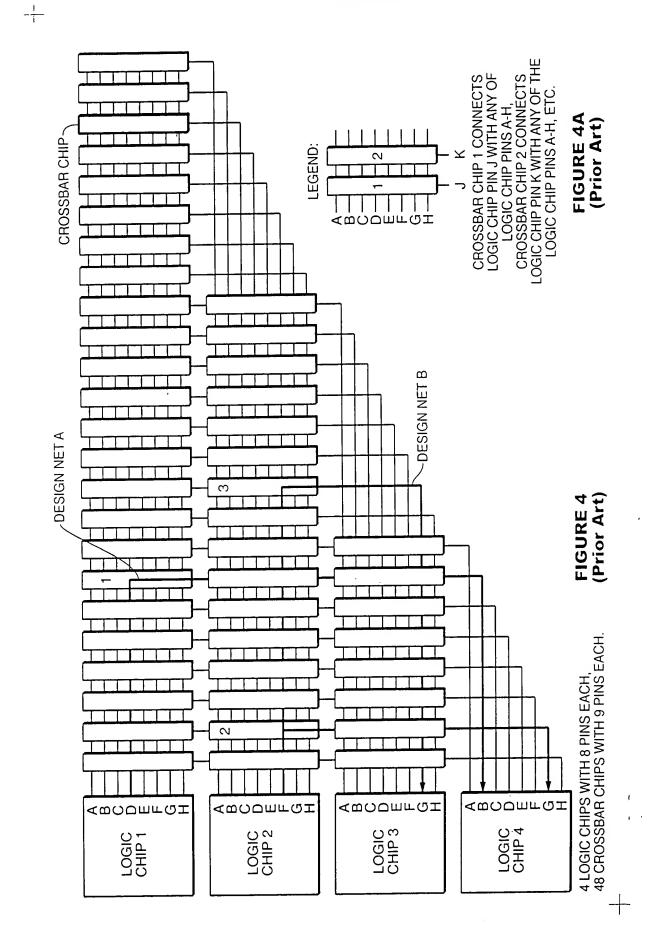


Sheet 2 of 25



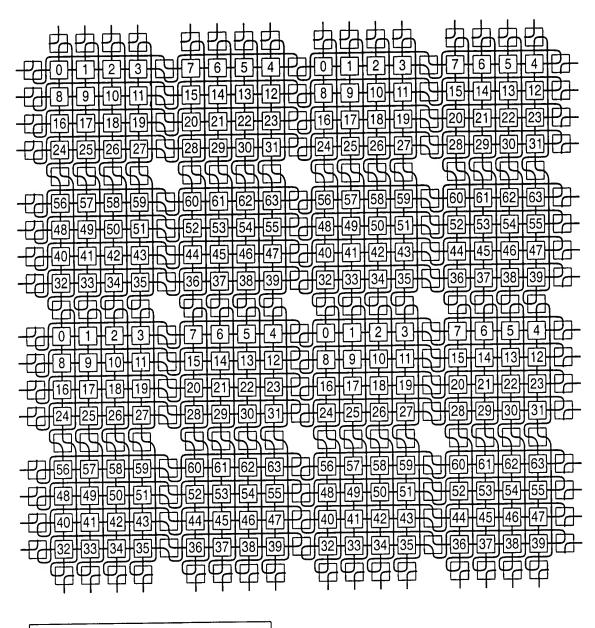


Sheet 3 of 25



Docket No.: 404332000101

Sheet 4 of 25



0 ~63 :PROCESSORS (NUMBERS REPRESENT LOGICAL ID)

THROUGH SWITCH

TURN SWITCH

FIGURE 5 (Prior Art)

Sheet 5 of 25

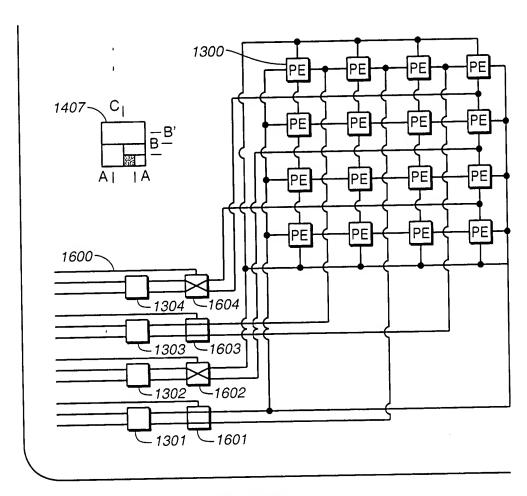


FIGURE 6 (Prior Art)

Sheet 6 of 25

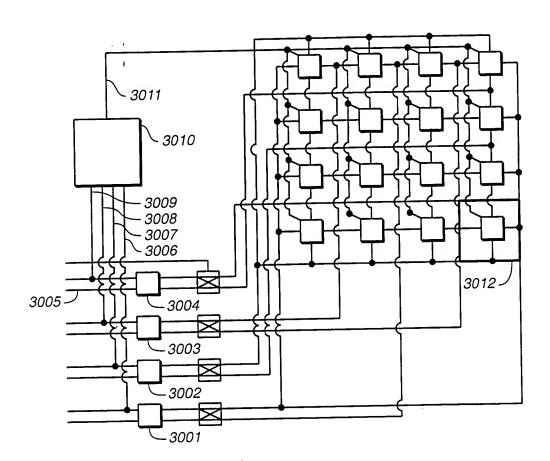
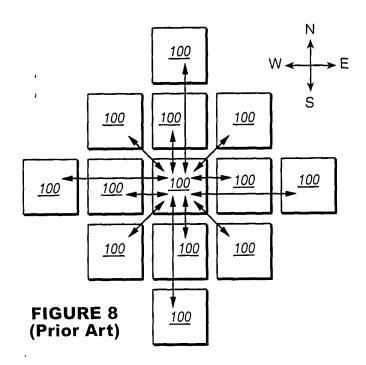
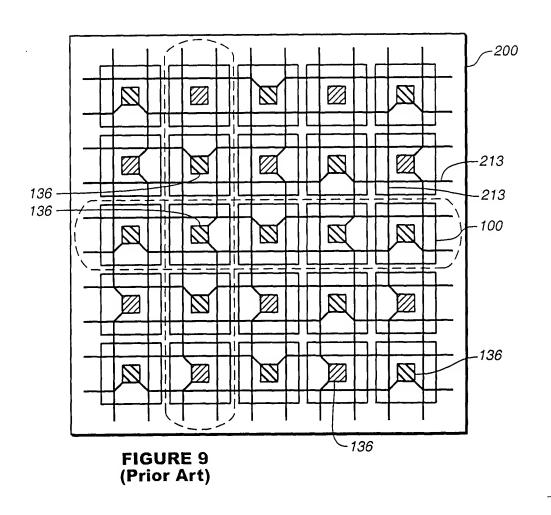


FIGURE 7 (Prior Art)

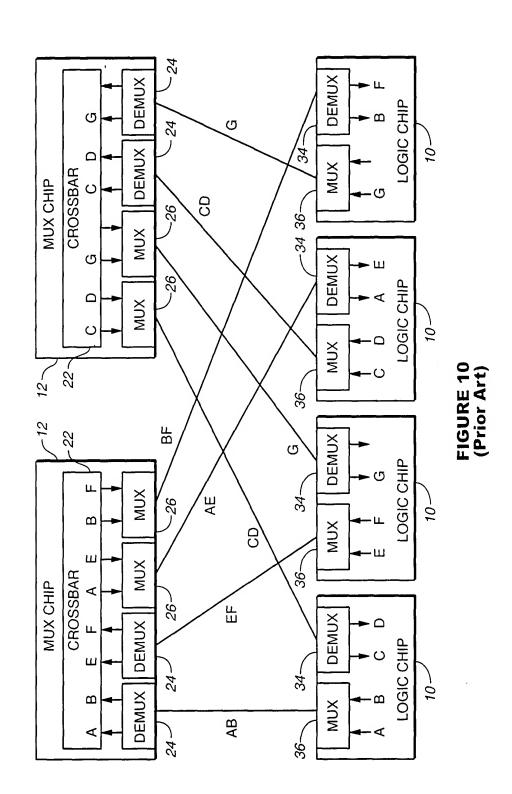
Sheet 7 of 25





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Sheet 8 of 25



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Docket No.: 404332000101

Sheet 9 of 25

FIGURE 11 (Prior Art)

Sheet 10 of 25

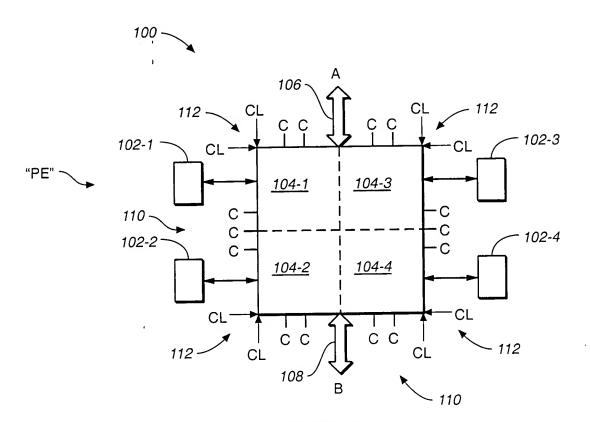


FIGURE 12

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Sheet 11 of 25

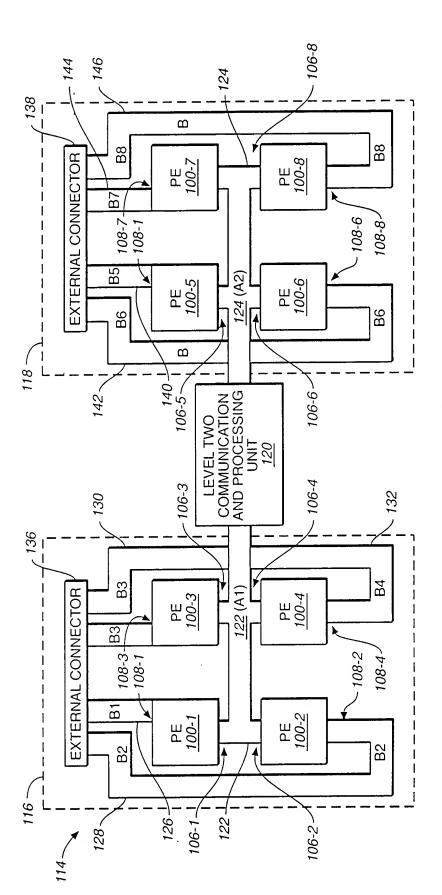
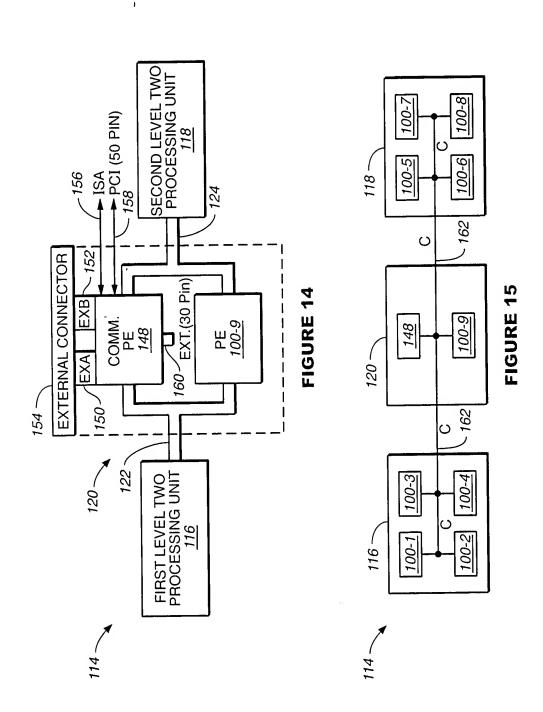


FIGURE 13

Sheet 12 of 25



Docket No.: 404332000101

Sheet 13 of 25

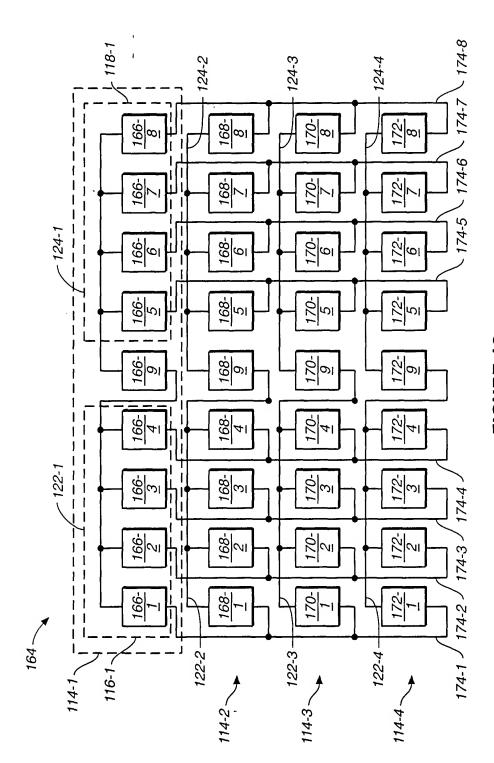


FIGURE 16

Sheet 14 of 25

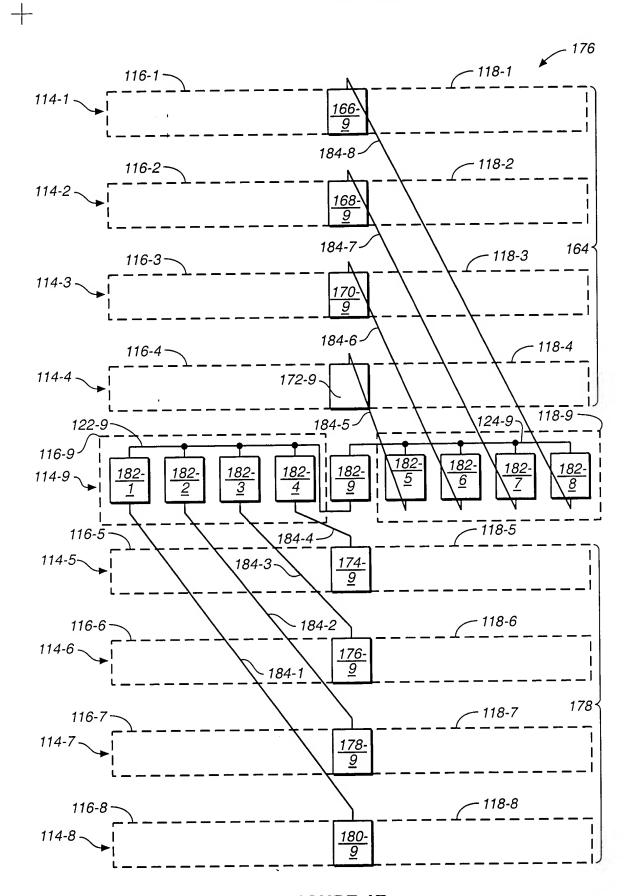


FIGURE 17

Docket No.: 404332000101

Sheet 15 of 25

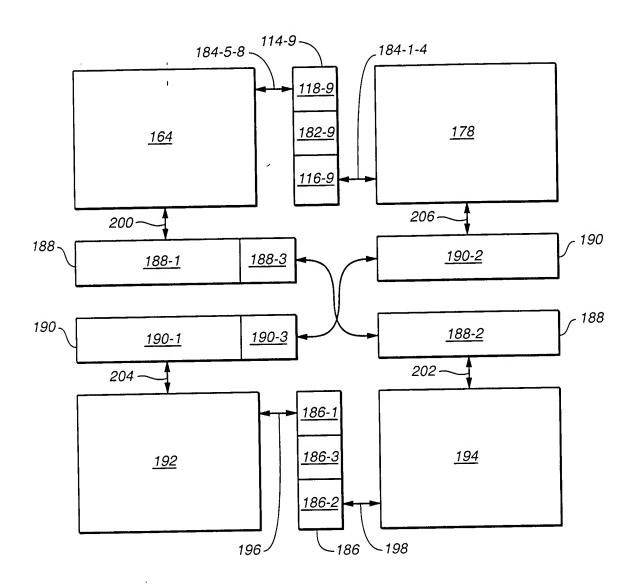
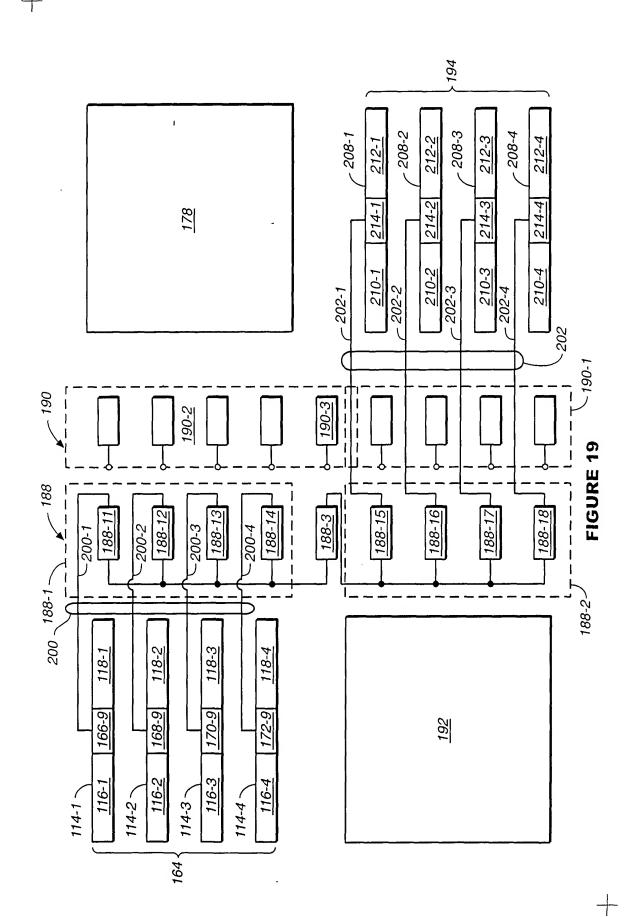
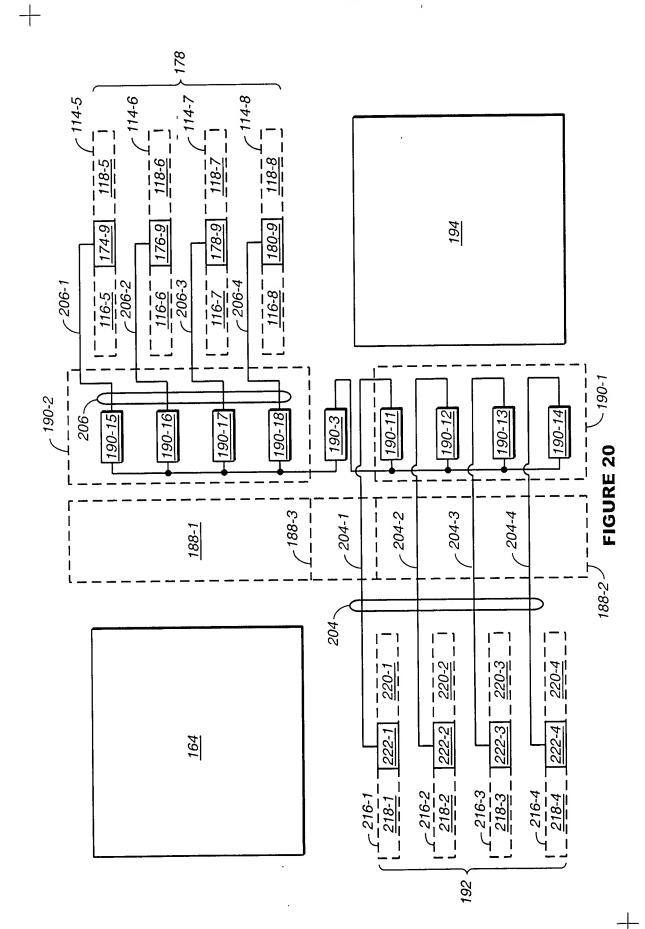


FIGURE 18

Sheet 16 of 25



Sheet 17 of 25



Docket No.: 404332000101

Sheet 18 of 25

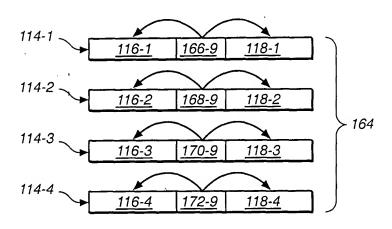
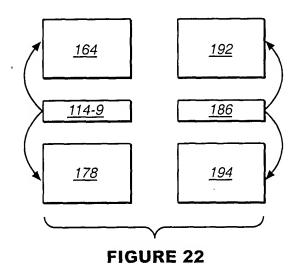


FIGURE 21



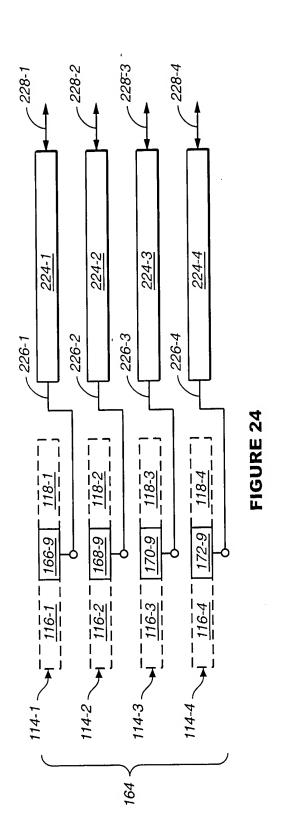
164 178

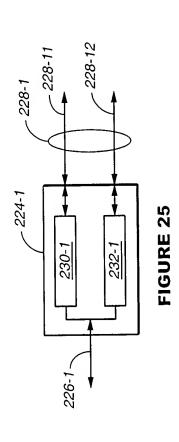
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194 192

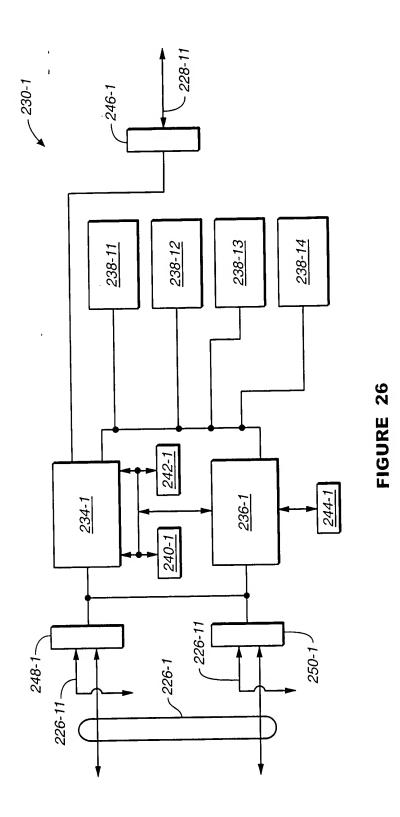
FIGURE 23

Sheet 19 of 25



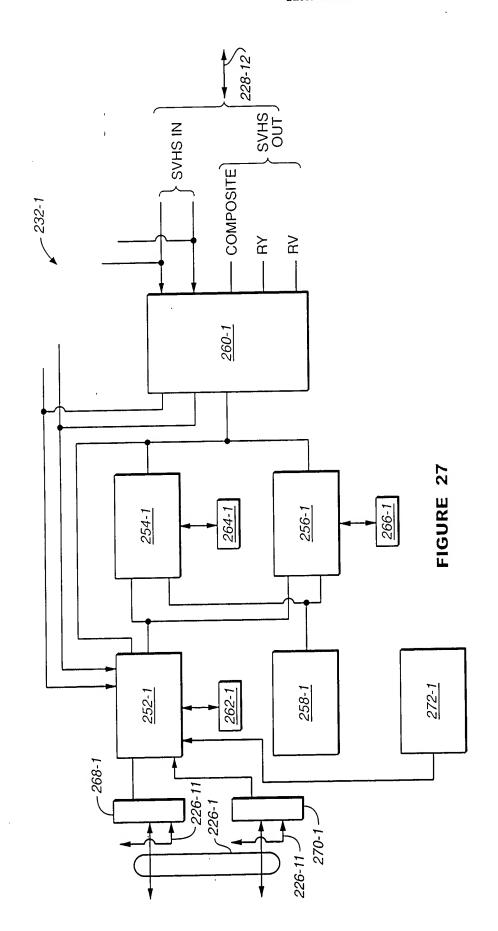


Sheet 20 of 25

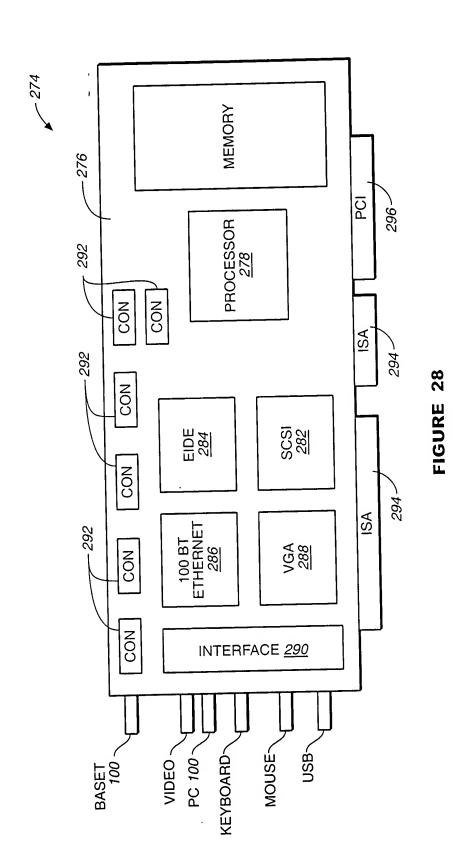


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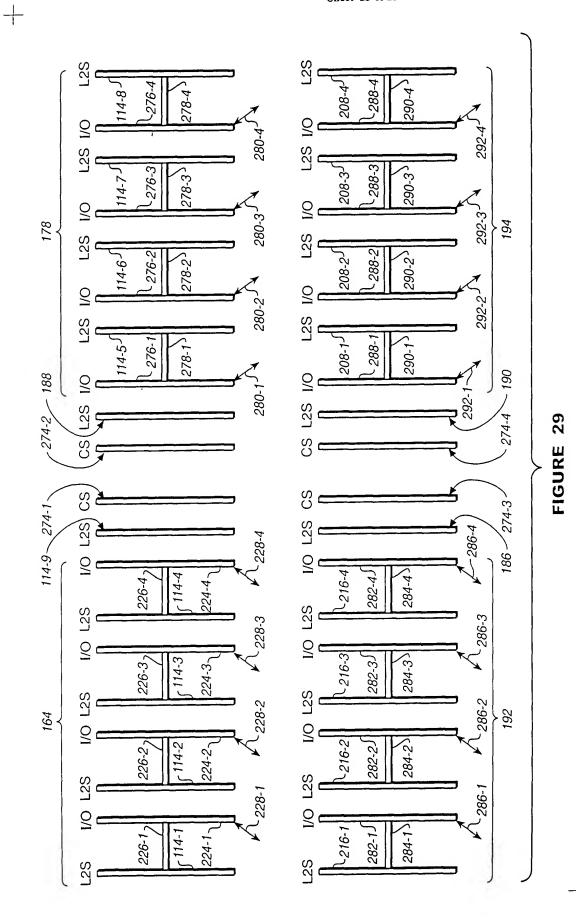
Sheet 21 of 25



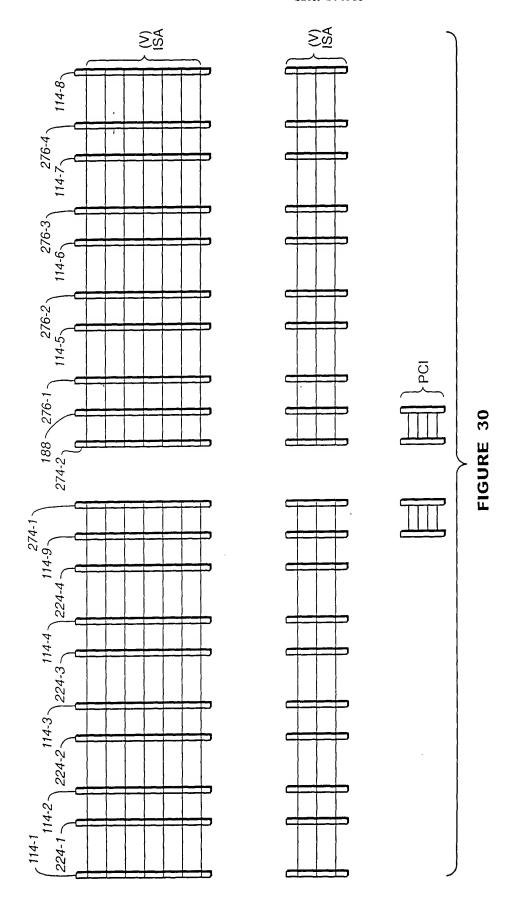
Sheet 22 of 25



Sheet 23 of 25

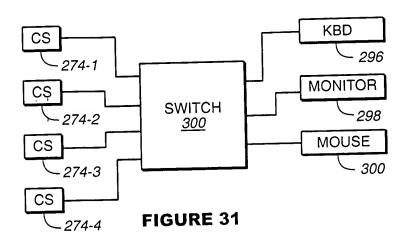


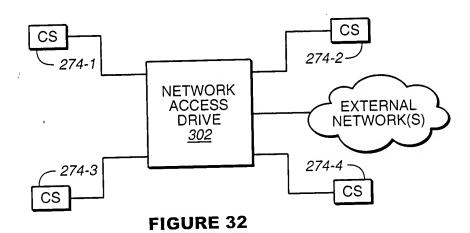
Sheet 24 of 25



Docket No.: 404332000101

Sheet 25 of 25





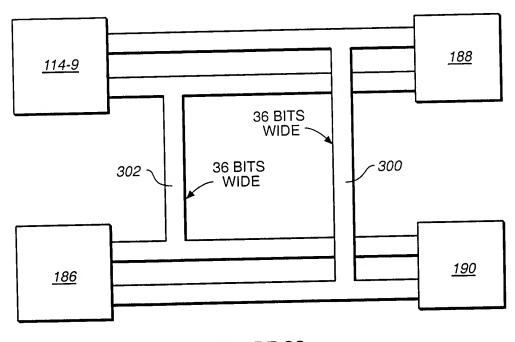


FIGURE 33